

Bharat Electronics Ltd., a Navratna Company and India's premier Professional Electronics Company requires outstanding Hardware & Software Engineers with FIRST CLASS in BE/B.Tech or equivalent, in **ELECTRONICS and COMPUTER SCIENCE** disciplines for Central D&E group in Bangalore, in the following domain areas:

Sl. No.	Domain Area	Designation	Grade	No. of posts	Pay scale (Rs.)	CTC (approx.) (P.A)	Upper age limit as on 01.05.2011	Minimum no. of years of relevant post qualification experience
1	Computing Platforms	Dy. Manager/ Manager	E-IV/ E-V	2	24900-3%-50500/ 29100-3%-54500	8.6 lakhs/ 10.4 lakhs	36 / 40	7 years / 10 years
		Sr.Engineer	E-III	3	20600-3%-46500	7.1 lakhs	32	3 years
2	Software Development	Manager/ DGM	E-V/ E-VI	5	29100-3%-54500 / 32900-3%-58000	10.4 lakhs/ 11.8 lakhs	40 / 45	10 years / 14 years
		Dy.Engineer/ Sr.Engineer	E-II/ E-III	14	16400-3%-40500/ 20600-3%-46500	5.7 lakhs/ 7.1 lakhs	28 / 32	1 year / 3 years

Candidates who had applied against our earlier advertisement in September 2010, need not apply. Only candidates with BE/ B.Tech in Electronics / Electronics & Communication/ Electronics & Telecommunication/ Telecommunication/ Communication/ Computer Science/ Computer Science & Engineering/ Computer Engineering from AICTE approved Colleges / recognized Universities, need apply. Engineering Degree holders in other disciplines / specializations need not apply. Reservation / Relaxation for SC/ST/OBC/PWD will be as per Government directives.

I) Post Qualification Experience for COMPUTING PLATFORMS FOR MANAGER / DY. MANAGERS

a) Design Skills (Hardware and Embedded Software):

- Experience in handling embedded system design and development.
- Knowledge of Project management tools.
- Should have expertise and should have guided the project team in some of the areas listed below:
 - Design and testing of Single Board Computers and Embedded systems.
 - Knowledge of various processor Architecture (RISC/CISC architecture, Intel ARM, PowerPC Freescale, Intel and Microchip Microcontroller architecture etc)
 - RTOS (Vx Works, QNX momentics) and Win CE.Net 5.0,
 - Digital System Design using FPGA (Altera & Xilinx), DSP (AD & TI) and IP cores embedded System on Chip (SOC).
 - Knowledge of interface standards like PCMCIA (PC Card), ExpressCard, PCI, PCIe, PC-104 Plus, USB, SPI, CPCI, VME, I2S/IIS, Gigabit Ethernet.
 - Knowledge of Serial Communication standards RS232/RS422 /RS485 and High Speed Serial communication protocols like HDLC, DDCMP, PPP.
 - Knowledge of Electronic circuit design and testing in Digital communication circuits - Digital Modulation / demodulation circuit design.

b) Areas of Experience (Preferred)

- Circuit design for computational platforms.
- High Speed PCB Design
- Knowledge in Networking Technologies.

FOR SENIOR ENGINEERS

a) Design Skills – Hardware and Embedded software (Should be well versed in some of the following areas):

- Experience in embedded system design and development.
- Knowledge of various processor Architecture (RISC/CISC architecture, Intel ARM, PowerPC Freescale, Intel and Microchip Microcontroller architecture etc)
- Experience in RTOS (Vx Works, QNX momentics) and Win CE.Net 5.0
- Knowledge of DSP architecture & programming (AD & TI)
- Knowledge of different memory technologies (DDR2/DDR3 SDRAM, Flash).
- Digital System Design using FPGA (Altera, Xilinx), DSP and IP cores embedded System on Chip (SOC).
- Conversant with Synthesis tools like Verilog, VHDL.
- Conversant with various simulation tools, emulators.
- Knowledge of interface standards like PCMCIA (PC Card), ExpressCard, PCI, PCIe, PC-104 Plus, USB, SPI, CPCI, VME, I2S/IIS, Gigabit Ethernet.
- Knowledge of Serial Communication standards RS232/RS422 /RS485 and High Speed Serial communication protocols like HDLC, DDCMP, PPP.
- Conventional Modem design and Soft Modem design using DSP, FPGA in PCMCIA or ExpressCard form factor.

b) Areas of Experience (Preferred)

- Circuit design for computational platforms (Single Board Computers).
- High Speed PCB Design
- Knowledge in Networking Technologies.

II) Experience for SOFTWARE DEVELOPMENT MANAGER / DEPUTY GENERAL MANAGER

a) Essential - Software Engineering Skills (Should be well versed in some of the following areas)

- Experience of working in Software projects from requirements, software Architecture
- Development, Testing, Delivery and support.
- Certified Project Management Professional
- Knowledge Of Software Architecture
- Knowledge of Project Management Tools.
- Knowledge of Software Development Life Cycles
- Analysis and Design of Algorithm
- Knowledge of IEEE12207 Standard, ISO/IEC 9126 Standard

- Knowledge of Software CMMI Level 5 processes and procedures
- b) Desired** - Should have guided teams in some of the disciplines listed below:
 - RTOS and embedded systems.
 - IDE's like visual studio, Work Bench, Eclipse etc.
 - Tracking, Image processing
 - JDBC, Relational Databases like ORACLE and MySQL.Postgre
 - Use of Static and dynamic testing tools like IBM Rational Robot, RTRT, Logiscope, Polyspace etc.
 - Experience in OOAD, UML, RUP and tools like IBM Rational Suite, IBM Rhapsody.
 - Web based applications development tools
 - Programming language Experience in C++, C# and Java.

c) Domain/Areas of Experience (Preferred)

- GIS Technologies
- Networking Technologies
- Communication Technology
- Real Time Applications
- Defence Systems like Radars, C4I, and Communication EW, Systems Displays etc.
- Software Metrics and Software Reliability

DEPUTY ENGINEER & SENIOR ENGINEER - Software Skills (Should be well versed in some of the following areas)

a) Essential

- Experience in Java EE (JSP / Servlets / EJB) and Java SE
- Programming Language experience in C++, C#, Java, Java based portlet development
- Experience in OOAD and working with XML
- Experience in working with an IDE similar to Eclipse, Rational Application developer, Netbeans
- Knowledge of JDBC, DBMS such as ORACLE, MySQL and Postgre
- Experience in RTOS and embedded systems.
- Experience in JDBC, Relational Databases like ORACLE and SQL.
- In depth Knowledge Of Software Development Life Cycles & Software Engineering Concepts

b) Desired

- Experience in use of Static and dynamic testing tools like Robot, RTRT etc.
- Experience in OOAD and UML with experience in tools like Rational Suite, IBM Rhapsody.
- Experience in Web based applications development tools and Web Servers
- Demonstrated experience in SOA and Web Services
- Knowledge of Spring framework or similar application development frameworks
- Knowledge of JDBC, relational databases, and SQL
- Knowledge of Software CMMI Level 5 processes and procedures
- Understanding and experience in GIS based Development
- Software Metrics and Software Reliability

c) Domain/Areas of Experience (Preferred)

- Networking Technologies
- Communication Technology
- Real Time Applications
- Defence Systems like Radars, C4I, and Communication EW, Systems Displays etc.

Note : Common to all

The candidate should be able to participate in outstation field / User trials

Applications should be submitted within 10 days of release of this advertisement, enclosing therein copies of the following documents:

- Biodata
- SSLC/Matriculation Certificate (as proof of age)
- Marks cards for having passed all semesters / years of BE / B.Tech/equivalent examination
- Final BE / B.Tech Degree certificate or Provisional Degree Certificate wherein the discipline and class obtained is clearly indicated. In the absence of this, the application will not be considered.
- Certificate from the previous employer/s regarding relevant post qualification experience. The tenure of service should be clearly mentioned.
- Caste / Disability certificate (if applicable) and other relevant certificates.
- No Objection Certificate, if employed with PSUs / Govt. organizations.

Applications from General / OBC candidates should be accompanied by a crossed Demand Draft drawn on any Scheduled Bank (preferably State Bank of India) for ₹500/- (DD of ₹200/- in case of SC/ST/PWD candidates) in favour of Bharat Electronics Limited, Bangalore. **Applications should be superscribed with the post and domain area applied for.**

Applications complete in all respects should reach **Manager (HR/C), Bharat Electronics Limited, Jalahalli Post, Bangalore – 560 013**, through post / courier within 14 days of release of this advertisement. Applications that are incomplete, not in the prescribed format, not legible or without the required enclosures will be summarily rejected, without assigning any reasons and no correspondence in this regard will be entertained.

Only short listed candidates will be called for the selections. Candidates called for interview will be reimbursed to and fro train fare, by the shortest route on production of the railway ticket receipts, as per Company rules.

These vacancies are identified to be filled up by external candidates only, through Direct Recruitment. Therefore, applications from internal candidates will not be considered. Deserving candidates may be extended relaxation in age / experience based on merit, at the discretion of Management.

Canvassing in any form will result in disqualification. BEL reserves the right to cancel the advertisement and / or the selection process. Only Indian nationals need apply.